

Refine Search

Search Results -

Terms	Documents
(bus near5 arbit\$6) and (grant\$3 same (concurrent\$2 or simultaneous\$2) same latenc\$3)	14

Database:

US Pre-Grant Publication Full-Text Database

US Patents Full-Text Database

US OCR Full-Text Database

EPO Abstracts Database

JPO Abstracts Database

Derwent World Patents Index

IBM Technical Disclosure Bulletins

Search:

L1

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Monday, June 19, 2006 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=PGPB; PLUR=YES; OP=OR</i>			
<u>L1</u>	(bus near5 arbit\$6) and (grant\$3 same (concurrent\$2 or simultaneous\$2) same latenc\$3)	14	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(bus near5 arbit\$6) and (grant\$3 same (concurrent\$2 or simultaneous\$2) same latenc\$3)	59

Database:

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L1

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Monday, June 19, 2006 [Printable Copy](#) [Create Case](#)

<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
	DB=PGPB,USPT,USOC; PLUR=YES; OP=OR		
<u>L1</u>	(bus near5 arbit\$6) and (grant\$3 same (concurrent\$2 or simultaneous\$2) same latenc\$3)	59	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(bus near5 arbit\$6) and (grant\$3 same (concurrent\$2 or simultaneous\$2) same latenc\$3)	11

Database:

US Pre-Grant Publication Full-Text Database

US Patents Full-Text Database

US OCR Full-Text Database

EPO Abstracts Database

JPO Abstracts Database

Derwent World Patents Index

IBM Technical Disclosure Bulletins

Search:

L2

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Monday, June 19, 2006 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L2</u>	(bus near5 arbit\$6) and (grant\$3 same (concurrent\$2 or simultaneous\$2) same latenc\$3)	11	<u>L2</u>
<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>			
<u>L1</u>	(bus near5 arbit\$6) and (grant\$3 same (concurrent\$2 or simultaneous\$2) same latenc\$3)	59	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(370/461 370/462 710/110 710/113 710/40 710/107 710/305 710/244 710/36 710/58 710/112 710/48 711/147 711/100 340/825.5 340/825.51).ccls.	9646

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L3

Refine Search

Recall Text

Clear

Interrupt

Search History

 DATE: Monday, June 19, 2006 [Printable Copy](#) [Create Case](#)

Set
 Name Query
 side by
 side

Hit
 Count
 Set
 Name
 result
 set

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L3 710/110,113,40,107,305,244,36,40,58,112,48;370/461,462;340/825.5,825.51;711/147,100.ccls. 9646 L3

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L2 (bus near5 arbit\$6) and (grant\$3 same (concurrent\$2 or simultaneous\$2) same latenc\$3) 11 L2

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L1 (bus near5 arbit\$6) and (grant\$3 same (concurrent\$2 or simultaneous\$2) same latenc\$3) 59 L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L1 and L3	37

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L4

Search History

DATE: Monday, June 19, 2006 [Printable Copy](#) [Create Case](#)

[Set](#)
[Name](#) [Query](#)
 side by
 side

[Hit](#)
[Count](#) [Set](#)
 [Name](#)
 result
 set

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

[L4](#) 11 and L3

37 [L4](#)

[L3](#) 710/110,113,40,107,305,244,36,40,58,112,48;370/461,462;340/825.5,825.51;711/147,100.ccls.

9646 [L3](#)

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

[L2](#) (bus near5 arbit\$6) and (grant\$3 same (concurrent\$2 or simultaneous\$2) same latenc\$3)

11 [L2](#)

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

[L1](#) (bus near5 arbit\$6) and (grant\$3 same (concurrent\$2 or simultaneous\$2) same latenc\$3)

59 [L1](#)

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L1 and L2	1

Database:

US Pre-Grant Publication Full-Text Database

US Patents Full-Text Database

US OCR Full-Text Database

EPO Abstracts Database

JPO Abstracts Database

Derwent World Patents Index

IBM Technical Disclosure Bulletins

Search:

L3

Refine Search

Recall Text

Clear

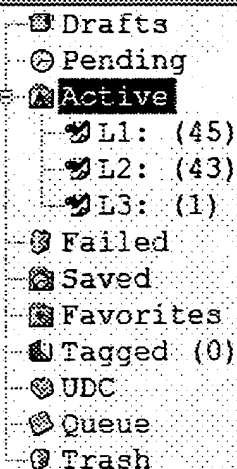
Interrupt

Search History

DATE: Monday, June 19, 2006 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>			
<u>L3</u>	l1 and L2	1	<u>L3</u>
<u>L2</u>	(bus near5 arbit\$6) and (grant\$3 same (concurrent\$2 or simultaneous\$2) same latenc\$3)	59	<u>L2</u>
<u>L1</u>	710/111.ccls.	112	<u>L1</u>

END OF SEARCH HISTORY



Search List **Phrases** Queue Clear

GBs USPAT ☒ **Phrases**

Default operator: OR ☒ **Highlight all hit terms initially**

[illegible]

 PDF form
 IS&R form
 Image
 Text
 HTML

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comment	Error	Definition	Errors
1	BRS	L1	45	(bus near5 arbit\$6) and (grant\$3 same (co	USPA T	2006/06/1 9 10:01				
2	BRS	L2	43	l1 and (request\$3 near5 bus)	USPA T	2006/06/1 9 10:02				
3	BRS	L3	1	l1 and (compar\$3 near5 latenc\$3)	USPA T	2006/06/1 9 10:02				

Start EAST - [...



Welcome United States Patent and Trademark Office

Search Results[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)[SUPPORT](#)

Results for "((bus and arbit*<in>metadata) <and> (grant*<in>metadata))<and> (concu..."

Your search matched **0** documents.A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order. [e-mail](#) [print](#) [friendly](#)

» Search Options

[View Session History](#)[New Search](#)

Modify Search

☐ Check to search only within this results set

» Key

Display Format:



Citation



Citation & Abstract

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

No results were found.

Please edit your search criteria and try again. Refer to the Help pages if you need assistance revising your search.

[Help](#) [Contact Us](#) [Privacy & Security](#) [IEEE.org](#)

© Copyright 2006 IEEE - All Rights Reserved



Welcome United States Patent and Trademark Office

Search Results

[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)[SUPPORT](#)

Results for "((bus and arbit*<in>metadata) <and> (grant*<in>metadata))"

Your search matched 13 of 1356463 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.
[e-mail](#) [printer friendly](#)

» Search Options

[View Session History](#)[New Search](#)

Modify Search

((bus and arbit*<in>metadata) <and> (grant*<in>metadata))

[Search](#)
☐ Check to search only within this results set

 Display Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

[view selected items](#)[Select All](#) [Deselect All](#)

- ☐ 1. **SAMBA-bus: A high performance bus architecture for system-on-chips**
 Ruibing Lu; Cheng-Koh Koh;
[Computer Aided Design, 2003. ICCAD-2003. International Conference on](#)
 9-13 Nov. 2003 Page(s):8 - 12
 Digital Object Identifier 10.1109/ICCAD.2003.1257568
[AbstractPlus](#) | Full Text: [PDF](#)(475 KB) [IEEE CNF](#)
[Rights and Permissions](#)
- ☐ 2. **Performance model for a prioritized multiple-bus multiprocessor system**
 John, L.K.; Yu-Cheng Liu;
[Computers. IEEE Transactions on](#)
 Volume 45, Issue 5, May 1996 Page(s):580 - 588
 Digital Object Identifier 10.1109/12.509909
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(724 KB) [IEEE JNL](#)
[Rights and Permissions](#)
- ☐ 3. **Resolving the Fairness Issues in Bus-Based Optical Access Networks**
 Bouabdallah, N.; Beylot, A.-L.; Dotaro, E.; Pujolle, G.;
[Selected Areas in Communications. IEEE Journal on](#)
 Volume 23, Issue 8, Aug. 2005 Page(s):1444 - 1457
 Digital Object Identifier 10.1109/JSAC.2005.851779
[AbstractPlus](#) | Full Text: [PDF](#)(1360 KB) [IEEE JNL](#)
[Rights and Permissions](#)
- ☐ 4. **Design of a CMOS buffered switch for a Gigabit ATM switching network**
 Mirfakhraei, N.;
[Solid-State Circuits. IEEE Journal of](#)
 Volume 30, Issue 1, Jan. 1995 Page(s):11 - 18
 Digital Object Identifier 10.1109/4.350200
[AbstractPlus](#) | Full Text: [PDF](#)(780 KB) [IEEE JNL](#)
[Rights and Permissions](#)
- ☐ 5. **Resolving the fairness issue in bus-based optical access networks**
 Bouabdallah, N.; Dotaro, E.; Ciavaglia, L.; Le Sauze, N.; Pujolle, G.;
[Communications Magazine. IEEE](#)
 Volume 42, Issue 11, Nov. 2004 Page(s):S12 - S18
 Digital Object Identifier 10.1109/MCOM.2004.1362545
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(289 KB) [IEEE JNL](#)
[Rights and Permissions](#)
- ☐ 6. **Performance model for a prioritized multiple-bus multiprocessor system**

Kurian, L.; Yu-Cheng Liu;
Parallel and Distributed Processing, 1994. Proceedings. Sixth IEEE Symposium on
 26-29 Oct. 1994 Page(s):577 - 584
 Digital Object Identifier 10.1109/SPDP.1994.346120
[AbstractPlus](#) | Full Text: [PDF](#)(488 KB) [IEEE CNF](#)
[Rights and Permissions](#)

 **7. A fluid flow tracking arbiter for input buffered ATM switch with heterogeneous port bandwidth**

Vishnu, M.;
Global Telecommunications Conference, 2003. GLOBECOM '03. IEEE
 Volume 6, 1-5 Dec. 2003 Page(s):3138 - 3143 vol.6
 Digital Object Identifier 10.1109/GLOCOM.2003.1258814
[AbstractPlus](#) | Full Text: [PDF](#)(256 KB) [IEEE CNF](#)
[Rights and Permissions](#)

 **8. The multiprocessor system with a fairly weighted global bus arbitration circuit**

Deshmukh, R.G.; Doi, S.;
Southeastcon '90. Proceedings. IEEE
 1-4 April 1990 Page(s):750 - 754 vol.2
 Digital Object Identifier 10.1109/SECON.1990.117917
[AbstractPlus](#) | Full Text: [PDF](#)(300 KB) [IEEE CNF](#)
[Rights and Permissions](#)

 **9. Point-to-point connectivity between neuromorphic chips using address events**

Boahen, K.A.;
Circuits and Systems II: Analog and Digital Signal Processing. IEEE Transactions on [see also Circuits and Systems II: Express Briefs. IEEE Transactions on]
 Volume 47, Issue 5, May 2000 Page(s):416 - 434
 Digital Object Identifier 10.1109/82.842110
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(888 KB) [IEEE JNL](#)
[Rights and Permissions](#)

 **10. Architecture and performance comparison of a statistic-based lottery arbiter for shared bus on chip**

Yan Zhang;
Design Automation Conference, 2005. Proceedings of the ASP-DAC 2005. Asia and South Pacific
 Volume 2, 18-21 Jan. 2005 Page(s):1313 - 1316 Vol. 2
 Digital Object Identifier 10.1109/ASPDAC.2005.1466585
[AbstractPlus](#) | Full Text: [PDF](#)(194 KB) [IEEE CNF](#)
[Rights and Permissions](#)

 **11. A generic programmable arbiter with default master grant**

Petro, F.; Hommais, D.;
Circuits and Systems, 2000. Proceedings. ISCAS 2000. Geneva. The 2000 IEEE International Symposium on
 Volume 5, 28-31 May 2000 Page(s):749 - 752 vol.5
 Digital Object Identifier 10.1109/ISCAS.2000.857610
[AbstractPlus](#) | Full Text: [PDF](#)(300 KB) [IEEE CNF](#)
[Rights and Permissions](#)

 **12. Some solutions for FIP network interconnection**

Saba, G.; Mammeri, Z.; Thomesse, J.P.;
Factory Communication Systems, 1995. WFCS '95. Proceedings. 1995 IEEE International Workshop on
 4-6 Oct. 1995 Page(s):13 - 20
 Digital Object Identifier 10.1109/WFCS.1995.482645
[AbstractPlus](#) | Full Text: [PDF](#)(620 KB) [IEEE CNF](#)
[Rights and Permissions](#)

 **13. Ordered arbiters**

Bystrov, A.; Yakovlev, A.;
Electronics Letters
 Volume 35, Issue 11, 27 May 1999 Page(s):877 - 879
[AbstractPlus](#) | Full Text: [PDF](#)(328 KB) [IEEE JNL](#)



Welcome United States Patent and Trademark Office

AbstractPlus

[BROWSE](#)[SEARCH](#)[IEEE Xplore GUIDE](#)[SUPPORT](#)
[View Search Results](#) | [Previous Article](#) |

[e-mail](#) [printer friendly](#)

Access this document

 Full Text: [PDF](#) (328 KB)

Download this citation

Choose [Citation & Abstract](#)Download [ASCII Text](#)» [Learn More](#)

Ordered arbiters

[Bystrov, A. Yakovlev, A.](#)

Dept. of Comput. Sci., Newcastle upon Tyne Univ., UK

This paper appears in: [Electronics Letters](#)

Publication Date: 27 May 1999

Volume: 35 , Issue: 11

On page(s): 877 - 879

ISSN: 0013-5194

INSPEC Accession Number: 6278050

Posted online: 2003-07-09 09:39:02.0

Abstract

The granting of requests in the order of their arrival is not satisfied in existing 1/n arbiters, such as token rings, tree arbiters and networks of two-way arbiters. The proposed solution stores pending grants in a FIFO, thus maximally preserving the request order, improves fairness and offers lower metastability rate.

Index Terms

Inspec

Controlled Indexing

[network interfaces](#)

Non-controlled Indexing

[FIFO storage](#) [asynchronous systems](#) [metastability rate reduction](#) [ordered arbiters](#) [pending grants storage](#) [request order preservation](#)

Author Keywords

Not Available

References

No references available on IEEE Xplore.

Citing Documents

No citing documents available on IEEE Xplore.

[View Search Results](#) | [Previous Article](#) |

[Help](#) [Contact Us](#) [Privacy & Security](#) [IEEE.org](#)

© Copyright 2003 IEEE -- All Rights Reserved

